

## BSC052N03S G-VB Datasheet

### N-Channel 30 V (D-S) MOSFET

#### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>a, e</sup>	$Q_g$ (Typ.)
30	0.003 at $V_{GS} = 10$ V	120	71 nC
	0.005 at $V_{GS} = 4.5$ V	90	

#### FEATURES

- Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested



#### APPLICATIONS

- Notebook PC Core
- VRM/POL



N-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	A
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Pulsed Drain Current	$I_{DM}$	250	mJ
Avalanche Current Pulse	$I_{AS}$	56	
Single Pulse Avalanche Energy	$E_{AS}$	60	
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25$ °C	A
		$T_A = 25$ °C	
Maximum Power Dissipation	$P_D$	$T_C = 25$ °C	W
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	41	50	°C/W
Maximum Junction-to-Case	$R_{thJC}$	0.7	0.9	

Notes:

a. Based on  $T_C = 25$  °C.

b. Surface mounted on 1" x 1" FR4 board.

c.  $t = 10$  s.

d. Maximum under steady state conditions is 90 °C/W.

e. Calculated based on maximum junction temperature. Package limitation current is 80 A.

Notes:

- a. Pulse test; pulse width  $\leq 300 \mu\text{s}$ , duty cycle  $\leq 2 \%$ .
- b. Guaranteed by design, not subject to production testing.

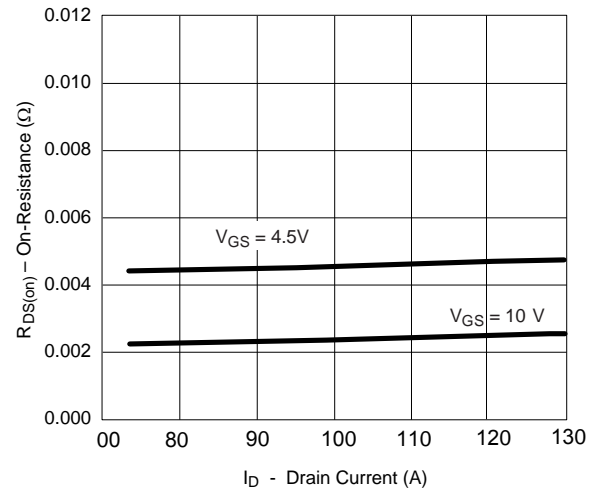
*Stresses beyond those listed under "Absolute Maximum Ratings" ma*

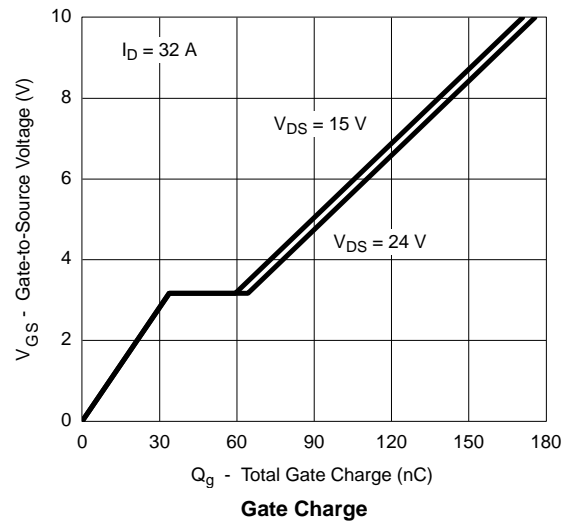
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**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

**Output Characteristics**

**Transfer Characteristics**

**Transconductance**

 **$R_{DS(on)}$  vs. Drain Current**

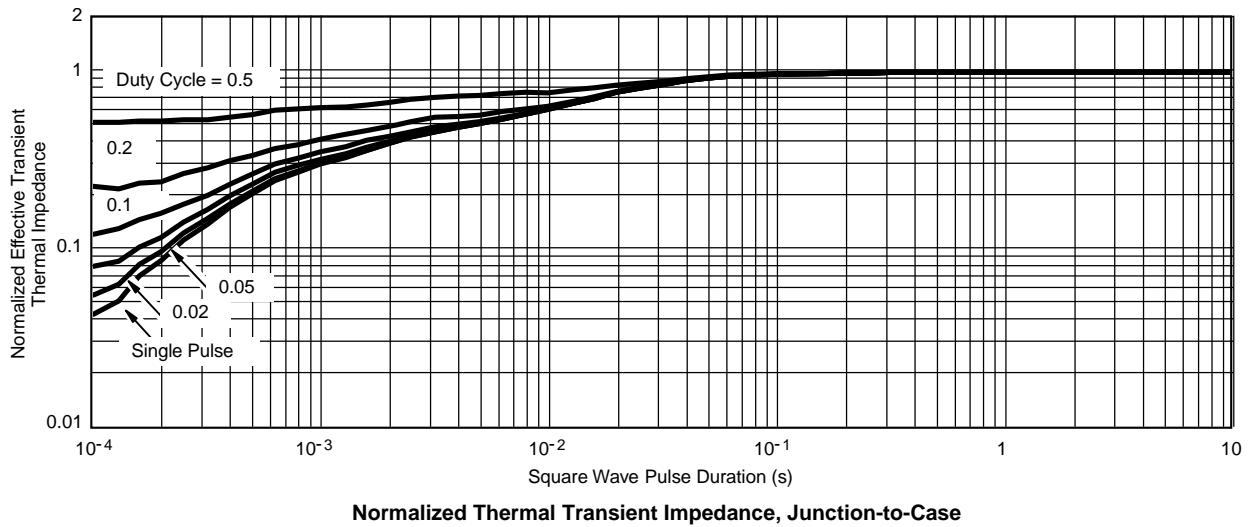
**Capacitance**

**Gate Charge**

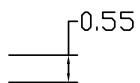
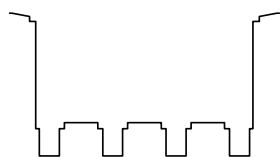
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)**On-Resistance vs. Junction Temperature****Forward Diode Voltage vs. Temperature** **$R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature****Threshold Voltage**\* $V_{GS} >$  minimum  $V_{GS}$  at which  $r_{DS(on)}$  is specified**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 175\text{ °C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





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